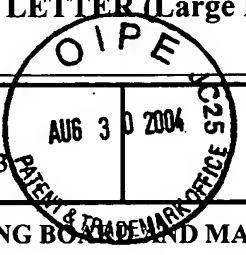
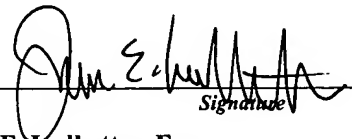


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AMENDMENT TRANSMITTAL LETTER (Large Entity)				Docket No. L8462.01108A	
Applicant(s): Yuichiro YAMADA, et al.					
Serial No. 10/616,981	Filing Date July 11, 2003		Examiner J. Clark	Group Art Unit 2815	
Invention: SEMICONDUCTOR CHIP, WIRING BOARD AND MANUFACTURING PROCESS THEREOF AS WELL AS SEMICONDUCTOR DEVICE					
<u>TO THE COMMISSIONER FOR PATENTS:</u>					
Transmitted herewith is an amendment in the above-identified application.					
The fee has been calculated and is transmitted as shown below.					
CLAIMS AS AMENDED					
	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST # PREV. PAID FOR	NUMBER EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE
TOTAL CLAIMS	14 -	42 =	0 x	\$18.00	\$0.00
INDEP. CLAIMS	8 -	15 =	0 x	\$86.00	\$0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					\$0.00
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT					\$0.00
<div style="display: flex; justify-content: space-between;"><div><p><input checked="" type="checkbox"/> No additional fee is required for amendment.</p><p><input type="checkbox"/> Please charge Deposit Account No. _____ in the amount of _____</p><p><input type="checkbox"/> A check in the amount of _____ to cover the filing fee is enclosed.</p><p><input checked="" type="checkbox"/> The Director is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 19-4375</p><p><input checked="" type="checkbox"/> Any additional filing fees required under 37 C.F.R. 1.16.</p><p><input type="checkbox"/> Any patent application processing fees under 37 CFR 1.17.</p></div><div style="text-align: right;"><p>Dated: August 30, 2004</p></div></div> <div style="margin-top: 20px;"><div style="display: flex; align-items: center;"><div style="text-align: center;"> _____ <small>Signature</small></div><div style="margin-left: 20px;"><p>James E. Ledbetter, Esq. Registration No. 28,732 Stevens, Davis, Miller & Mosher, LLP 1615 L Street, N.W., Suite 850 Washington, DC 20036 Tel: 202-785-0100 Fax: 202-408-5200</p></div></div></div>					
<div style="border: 1px solid black; padding: 5px; margin-bottom: 10px;">I certify that this document and fee is being deposited on _____ with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.</div> <div style="border: 1px solid black; padding: 5px; margin-bottom: 10px; text-align: center;">Signature of Person Mailing Correspondence</div> <div style="border: 1px solid black; padding: 5px; text-align: center;">Typed or Printed Name of Person Mailing Correspondence</div>					
CC:					



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Inventors: Yuichiro YAMADA, et al.

Art Unit: 2815

Serial No.: 10/616,981

Examiner: J. Clark

Filed: July 11, 2003

For: SEMICONDUCTOR CHIP, WIRING BOARD AND MANUFACTURING
PROCESS THEREOF AS WELL AS SEMICONDUCTOR DEVICE

AMENDMENT AND RESPONSE TO QUAYLE ACTION

Assistant Commissioner of Patents
Washington, DC 20231

Sir:

In response to the office action of June 29, 2004, please
amend the above-captioned application as follows: